

Features

- Maximum Sample Rate
 - Max. 20 GHz
 - 1.25 / 2.5 / 5 / 10 / 20 GHz options
- Number of Channels: total 34 CH
- Single-ended channels (31 CH)
 - Per-pin termination level
 - Per-pin threshold level
 - Per-pin timing delay
- Differential channels (3 CH)
 - 1 Differential clock input
 - 2 Differential strobe input
- Analysis mode
 - Timing mode
- Acquisition Memory
 - Max. 512 GB (standard 128 GB)
 - Full memory depth up to 20 GHz
 - Hardware data compression
- Hysteresis settings for all channels
- Host Interface: PCIe Gen3 x8
- Target application
 - High Speed NAND Device

JKI JLA534F-20G

NAND Protocol Analyzer



Overview

The JKI JLA534F protocol analyzer system is the industry's highest performance NAND protocol analyzer with deep acquisition memory depth up to 512GB and highest sampling rate up to 20GHz. Our high-speed and powerful memory trace solutions enable you to debug and validate NAND based memory systems up to 2.4 Gb/s

Large Memory for Data Capturing

Large memory depth up to 512GB allows you to debug very complex problems by long capturing time.

Ultra-fast Sampling Speed

Ultra-fast sampling speed up to 20 GHz allows you to capture data rate up to 2.4 Gb/s for all channels without any extra options.

Probe Solution

To minimize signal degradation in the probe system, the analog module is separated from the digital module and placed as close as possible to the device.

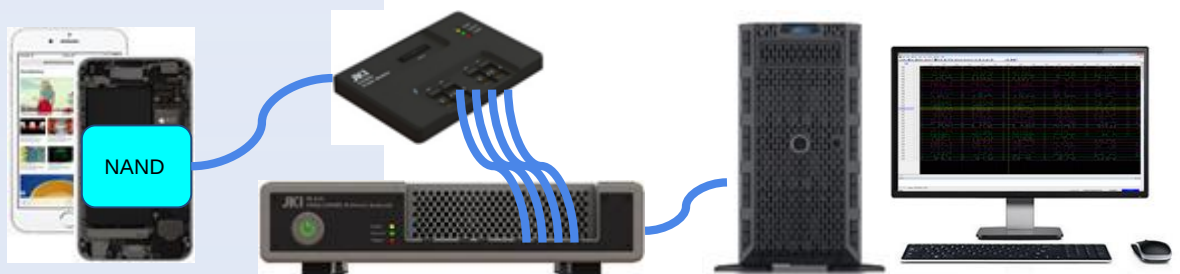
Interposer Solution

We provide variety of interposer and connecting solutions:

System Under Test

Protocol Analyzer

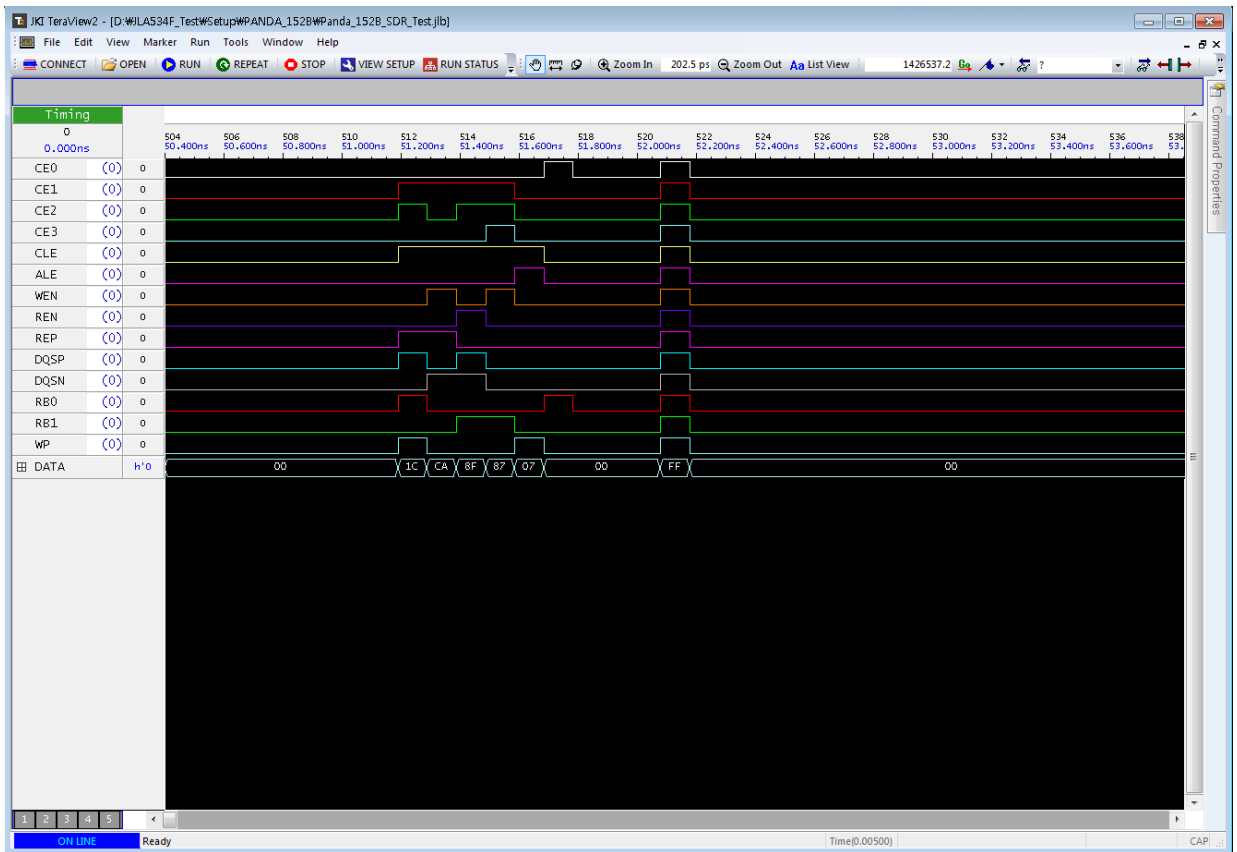
Storage Server



Software Features

- ❖ Capturing, Navigating, and Analyzing DDR memory protocol through fast TeraView 2.0 GUI.
- ❖ Easy setup and configuration window.
- ❖ Software based timing mode eye diagram.
- ❖ Programmable packet decoder and APIs for user post-analysis tool development.

New TeraView 2.0 Wave & List View



Channel Specification	
Number of Channels	34 (31 single-ended + 3 differential)
Input Voltage Range	-0.2V to 1.2V (analyzer input)
Minimum Input Voltage Swing	40 mV (analyzer input)
Termination (Vtt) Voltage Range	0V to 1.2V (analyzer input)
Termination (Vtt) Voltage Resolution	1 mV
Threshold (Vref) Voltage Range	0V to 1.2V (analyzer input)
Threshold (Vref) Voltage Resolution	1 mV
Hysteresis (Vhys) Voltage Range	0 ~ 50 mV
Timing Delay Range	-2ns to 2ns
Timing Delay Resolution	50 ps @20 GHz
Input Impedance	50 Ohm (analyzer input) 250 Ohm (5:1 probe) 500 Ohm (10:1 probe)

Acquisition Specification	
Sample Rate	1.25 / 2.5 / 5 / 10 / 20 GHz
Analysis Mode	Timing mode, Filter mode, Eye mode
Acquisition Memory	Max. 512 GB (standard 128 GB)
Host Interface	PCIe Gen3 x8
Data Compression	Hardware compression
Data De-compression	Software de-compression

Trigger Specification	
Trigger Pattern	E(Either edge), R(Rising edge), F(Falling edge), H(High), L(Low), X(Don't care) for all channel
Trigger Sequence	5 burst trigger
Trigger Action	Start capture
Trigger Rate	1.25G
Pre-trigger Size	Max. 128Gbytes (depends on RDIMM Size)

Filtering Specification	
Filter Pattern	E(Either edge), R(Rising edge), F(Falling edge), for all channel
Filtering Rate	1.25G
Filtering Size per a event	8 bytes including raw data and time information

Environmental and physical	
Power Consumption	Max. 150W
Operation Temperature (nom)	0 to +40 dec C
Humidity (nom)	0 to 80% relative humidity
Dimensions (W x H x D)	347 mm x 290 mm x 74 mm
Weight	4.7kg

JLA534F NAND Protocol Analyzer System Configuration - Recommended

Model	Qty	Description
JLA534F-20G	1	JLA534F NAND Protocol Analyzer (128GB – Standard)
- Option 256G/512G	1	256GB/512GB GB memory option
P21034A	1	Soft Touch Connectorless Probe, Single-ended with 34 channel
PCIe-HIB38	1	PCIe host adapter (Gen3 x8)
PCIe-0802	1	PCIe cable 2m (Gen3 x8)